

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

CATALASAN *et al.*

Appl. No.: 10/697,889

Filed: October 31, 2003

For: **Method for Manufacturing a
Memory Cell for Modification of
Revision Identifier in an Integrated
Circuit Chip**

Confirmation No.: 9019

Art Unit: 2891

Examiner: Menz, Douglas M.

Atty. Docket: 1875.4360003

Amendment and Reply Under 37 C.F.R. § 1.111

Mail Stop Amendment

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

In reply to the Office Action dated June 26, 2007, (PTO Prosecution File Wrapper Paper No. 20070623), Applicants submit the following Amendment and Remarks. Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper. Remarks and Arguments begin on page 6 of this paper. It is not believed that extensions of time or fees for net addition of claims are required beyond those that may otherwise be provided for in documents accompanying this paper. However, if additional extensions of time are necessary to prevent abandonment of this application, then such extensions of time are hereby petitioned under 37 C.F.R. § 1.136(a), and any fees required therefor (including fees for net addition of claims) are hereby authorized to be charged to our Deposit Account No. 19-0036.